



Position Control Unit DVP-01PU Instruction Sheet

WARNING

- A This Instruction Sheet only provides descriptions for installation, wiring and trial run. For further infromation, please refer to special module of PLC Application Manual.
- A Please turn off power before wiring.
- ${
 m \AA}$ This is an OPEN TYPE PLC. The PLC should be kept in an enclosure away from airborne dust, humidity, electric shock risk and vibration. Also, it is equipped with protective methods such as some special tools or keys to open the enclosure, in order to prevent hazard to users or damage the PLC.
- ${
 m
 m
 m A}$ Do NOT connect the AC input power to any of the input/output terminals, or it may damage the PLC. Check all the wiring prior to power up.

INTRODUCTION

2

1

2.1 Model Description and Peripherals

DVP-01PU (position control unit) is mainly applied to the speed/position control of step/servo driven system. The maximum output pulse can be up to 200 KPPS, and built-in various route control modes. The DVP-PLC EH series can read/write DVP-01PU via FROM/TO instrucitons. There are 54 CRs(Controlled Register) in DVP-01PU and 16 bits for each register. The 32-bits data is composed of 2 continuous CR number.

Nameplate IODEL : DVP01PU-H PLC product mode Power input -WER INPUT : 24VDC 3W MAX TPUT MODULE : Differential Line Driv Output modul Bar code, serial No, edi

Model & Serial Number



2.2 Product Profile and Outline (LED Indicator and Terminal Block)



Unit: mm

| 1.DIN rail track (35mm) | 2. Mounting wire to connect extension module/extension unit |
|---|---|
| 3.Model name | 4. Status Indicator (Power, Run and ERROR) |
| 5.DIN rail clip | 6.Terminal |
| 7.Mounting hole | 8.Terminal layout |
| 9.Extension port to connect extension module/unit | 10. RS-485 communication port |

LED Display

| POWER | : Power indicator, +5V internal power | START | : Start input |
|-------|--|----------|---|
| LV | : Low voltage indicator (lit when external | STOP | : Stop input |
| | input power is lower than 19.5V) | DOG | : DOG (near point signal) input |
| ERROR | : Error occurred indicator. It will blink when | FP | : CW pulse output |
| | CR#44 is not 0. | RP | : CCW pulse output |
| LSP | : Right limit input indicator | ΦA | : A-phase input of manual pulse generator |
| LSN | : Left limit input indicator | ΦВ | : B-phase input of manual pulse generator |
| PG0 | : Zero signal input indicator | CLR | : Output clear signal |
| | | | |

Input/Output Terminal

| Description | Terminal name | Explanation | Response character |
|--------------|----------------------------|---|-----------------------|
| Power supply | +24V, 0V | Power input/DC24V (-15~+20%) Current consumption 100mA | - |
| | START | Start input terminal | 15ms/50m |
| | STOP | Stop input terminal | 15ms |
| | LSP / LSN | Input right/left limit | 1ms |
| | Φ Α+ , Φ Α- | A-phase terminal (+, -) of manual pulse generator input (line driver input) | 200KHz |
| Input | Φ B+ , Φ B - | B-phase terminal (+, -) of manual pulse generator input (line driver input) | 200KHz |
| | PG0+, PG0- | Zero signal input terminal +, - (line driver input) | 1ms |
| | DOG | Offers two different functions depending on operation mode. (1) It is near-point signal in zero return mode. (2) It is start signal on interrupt 1st or interrupt 2nd speed mode. | 1ms |
| | S/S | Signal common terminal of these Inputs (START, STOP, DOG, LSP, LSN) | - |
| | CLR+, CLR- | Clear signal (clear signal of internal error counter for Servo drive) | 130ms |
| Output | FP+, FP- | FP/RP mode: CW pulse output I/O mode: Output pulse AB-phase mode: A-phase output | 200KHz |
| | RP+, RP- | FP/RP mode: CCW pulse output I/O mode: direction output AB-phase mode: B-phase output | 200KHz |

2.3 Wiring



1. Please wire I/O by O-type or Y-type terminals as the specification shown at left. The torque of PLC terminal screw should be 5~8 kg-cm (4.3~6.9 in-lbs).

- ^{nals} 2. I/O signal wires or power supply should not run through the same multi-wire cable or conduit.
- 3. Use copper conductors only, 60°C.

Input/Output Circuit



SPECIFICATIONS

3.1 Function Specifications

3

| Item | Description |
|----------------------------------|---|
| Power supply | DC24V(-15% \sim +20%), Current consumption 140 \pm 30mA Power is supplied from EH series or external power supply. |
| Max. number of connected axes | 8 units (axes); (All I/O points are not occupied. There are 8 special extension units at most to connect to EH series.) |
| Distance instruction | Distance value is set by CR. 1. Setting value: -2, 147,483,648~+2,147,483,647; 2. Selectable unit: um, mdeg, 10 ⁻⁴ inch, Pulse; 3. Selectable rate: 10 ⁰ , 10 ¹ , 10 ² , 10 ³ ; 4. Selectable position: absolute and relative position instruction |
| Speed instruction | Speed value is set by CR. 1. Setting value: -2,147,483,648~+2,147,483,647 (conversion value of 10~200KPPS pulse); 2. Unit selectable: pulse/s, cm/min, 10deg/min, inch/min |
| | |

| Item | |
|--------------------------------------|--|
| External output | Photo Outp Outp 20m/ |
| External input | Photo Input 10%, Input Input |
| Pulse output format | Three outpu |
| Position program & data transmission | The 32-bi ~ CR |
| Connect to DVP-PLC in series | When to 7. |

3.2 Other Specification

| Operation/Storage | 1. Op 2. Sto |
|-----------------------------|-----------------|
| Vibration/Shock immunity | Stand |
| Antistatic spec. | All pl |
| | |





| b1 | b0 | Unit | | |
|----|----|------------|------|---|
| 0 | 0 | Motor | uo | р |
| 0 | 1 | Machine | siti | р |
| 1 | 0 | Combined | οд | р |
| 1 | 1 | Complitieu | þ | |
| | | | See | |
| | | | Ś | |

| bit # | ŧ | | | | | | |
|----------|----|----|-------|-------------|--------------|---------|------|
| 6 | | W | hen b |)[6 |]=0: | posit | ive |
| <u> </u> | | W | hen b | 0[0 0[7 |]=1:]=0: | nega | tiv |
| 7 | | Ŵ | hen b | 5[7 |]=1: | nega | tiv |
| 8 | | W | hen b | 8]0 |]=0: | zero | re |
| Ŭ | | is | exect | ute | ed to | the c | lir |
| 9 | | W | hen C | CM | / rur | nning | is |
| 10 | | W | hen b | b [1 | 0]=(|): DO | G |
| | | ar | inte | erru | upt 2 | 2nd sp | be |
| | | W | hen b | p[1 | 1]=0 |): pos | itiv |
| 11 | | W | hen b | 0[1 | 1]=1 | : neg | ati |
| | | VV | nen I | n z | ero | returr | 1 [|
| 12 | | W | hen b | 0[1 | 2]=(|): trap | ez |
| 13 | | W | hen b | b [1 | 3]=(|): 15n | าร |
| 14 | | W | hen b | [] | 4]=(|): pos | iti |
| | | VV | nen b | 0[1 | 4]=' | I: neg | at |
| 15 | | | hen b | 1 | 5]=(|): pos | |
| | | vv | nen u | ηı | 5]= | r. neg | a |
| #7 | \$ | ¥6 | H'413 | 32 | ~ | R/W | ſ |
| #9 | \$ | ¥8 | H'413 | 34 | ~ | R/W | |
| #11 | # | 10 | H'413 | 36 | ~ | R/W | |
| #13 | # | 12 | H'413 | 38 | ~ | R/W | - |

Description

o coupler is for insulation and there are LED indications for all output/input signals uts: FP and RP (line driver output 5V) out: CLR is the type of NPN open collector transistor output (5~24VDC, less than

t point: START, STOP, LSP, LSN, DOG(contact or open collector transistor, 24VDC \pm 5±1mA)

ts: ΦA , ΦB (line driver or open collector transistor, 5~24VDC, 6~15mA) PG0(line driver or open collector transistor, 5~15VDC, 6~15mA) e selectable modes: Pulse/Dir, FP(CW)/RP(CCW), A/B (all modes are line driver

DVP-PLC EH series can read/write data in CR via FROM/TO instrucitons. The its data is composed of 2 continuous CR number. The range of 16-bits CR is CR#0 \$#53.

n DVP-01PU modules are connected to an MPU, the modules are numbered from 0 0 is the closest and 7 is the farthest to the MPU. 8 modules is the max and they do not occupy any digital I/O points of the MPU.

Environmental specifications

peration: 0° C ~55 $^{\circ}$ C (Temperature), 50~95% (Humidity), pollution degree 2 orage: -25°C~70°C (Temperature), 5~95% (Humidity

dard: IEC1131-2, IEC 68-2-6 (TEST Fc)/ IEC1131-2 & IEC 68-2-27 (TEST Ea)

laces between terminals and ground comply with the spec.

CR (Controlled Register)

| | DVP-0 | 1PU | Posi | tion | Cont | rol U | nit | | | | | | | | |
|--|---|---|-----------------------|-------------------------|----------------------------|------------------------------|----------------------|------------------------------|------------------------------|--------------------|--------------------|------------|---------------------|-----------------------|--------------|
| С | ontent | | | | | | S | ettin | g Ra | inge | • | | | | |
| Мо | odel No. | System setting, Read-only (The model number of DVP- H'0110.) | | | | | | | 0VP-01 | PU is | | | | | |
| Pulse otate revo | required to motor for 1 plution (A) | Range: 1 ~ +2,147,483,647 PPS/REV, factory setting: Pulse/Revolution (PLS/REV) | | | | | | | ng: 2,0 | 00 | | | | | |
| Macl ange rot revo | Rai Fac | nge: ctory | 1 ~ sett | +2,14 ing: 1 | 47,48 1,000 | 33,64) (un | 17 un it*1/F | iit/RE REV) | EV, | | | | | | |
| | | b15 | b14 | b13 | b12 | b11 | b10 | b9 | b8 | b7 | b6 | b | 5 b4 | b3 b2 | b1 b0 |
| Parameter setting Factory setting: H'0000 | | STOP input polarity | START input polarity | START response time | Acceleration curve options | DOG polarity | DOG trigger time | Pulse direction | Zero return direction | LSN input polarity | LSP input polarity | - | Pulse output format | Position rate setting | Unit setting |
| lotor | Combined | Mac | hine | Ł | o3 b2 | 2 | Posi | tion | rate | | b5 | b4 | Ρι | ulse ou | tput |
| ulse | | <u>u</u> | | | 0 0 | | 3 | 10 ⁰ | 9 | _ | 0 | 0 | | FP + F | P |
| ulse | m de | n | | | 0 1 | | | 10 ¹ | | | 0 | 1 | Puls | | ection |
| uleo | 10 ⁻⁴ ir | -y hch | | -11- | | - | | 10 ² | | | 1 | 0 | 1 013 | | SCIIOT |
| puls | se/sec | cm/r | nin | | 1 1 | | | 10 ³ | | | 1 | 1 | A/B | Phase | pulse |
| pul | se/sec 1 | 0deg | /min | | | | | | | | | | | | |
| pul | se/sec | inch/ | min | | | | | | | | | | | | |
| pulse/sec cm/min 1 1 10° 1 1 10° 1 1 1 10° 1 | | | | | | | | | | | ≥turn 1st | | | | |
| aximu | um speed V _m | ^{iax} F | Rang Facto Rang | je: 0 ory s je: 0 | ~ +2 etting ~ +2 | ,147 <u>g: 20</u> ,147 | ,483 0,00 ,483 | ,647 <u>0 uni</u> ,647 | unit* <u>t*1</u> unit* | '1 (1 '1 (0 | 0~ | 20 | ok pf | PS) *2 S pulse | • |
| DIas | speeu V _{bias} | t | rans | fer v | alue) | *2F | actor | y se | tting: | 0 u | init* | 1 | | | |
| JOG | speed V_{JOG} | F | Rang | je: 0 fer v | ~ +2 alue) | ,147 *2F | ,483 actor | ,647 <u>y se</u> | unit* tting: | 1 (1 5,0 | 0~ 00ι | 20 unit | 0K PF *1 | PS puls | e |
| ero re | turn speed V | , F RT t | ≺ang rans | je: 0 fer v | ~ +2 alue) | ,147 *2F | ,483 actor | ,647 'v se' | unit* ttina: | 1 (1 50 | 0 ~ 000 | 20 un | UK PF it*1 | -S puls | e |

| - | #14 | H'413A | ~ | R/W | Zero return deceleration sp | beed | Range: 0 ~ +2,147,483,647 unit*1 (10 ~ 200K PPS pulse transfer value) *2, factory setting: 1,000 unit*1 | | | | | |
|---|---|--|--|--|---|---|---|--|--|--|--|--|
| | #16 | <i>t</i> 16 H'413C ✓ R/W The number of PG0 in zero return mode N Range: 0~+32,767 PLS, factory setting: 0 PLS | | | | | | | | | | |
| | #17 | H'413D | ~ | R/W | The number of p | oulse ode P | Range: -32,768 ~+32,767 PLS, factory setting: 0 PLS | | | | | |
| | #18 | H'413E | ~ | R/W | Zero return mo | ode | b0: Zero return mode, b1: detect DOG falling-edge in zero | | | | | |
| | | | | | | | | | | | | |
| bit # | 0 b[0]=0: normal mode, b[0]=1: override mode | | | | | | | | | | | |
| 1 | 1 b[1]=0: DOG falling-edge detecting is on in zero return mode. b[1]=1: DOG falling-edge detecting is off in | | | | | | | | | | | |
| 100 | ze | ro returi | n ma | bae. | | (115) | | | | | | |
| 720 | #19 #21 | H'413F H'4141 | ✓ ✓ | R/W R/W | Acceleration tim | g (HP) e T _{acc} | Range: $0 \sim \pm 999,999$ unit ⁻¹ , factory setting: 0 unit ⁻¹ Range: 10 ~ +32,767 ms, factory setting: 100 ms | | | | | |
| | #22 | H'4142 | ✓ | R/W | Deceleration tim | e T _{dec} | Range: 10 ~ +32,767 ms, factory setting: 100 ms | | | | | |
| ‡ 24 | #23 | H'4143 | \times | R/W | Target position (| I) P(I) | (-2,147,483,648 ~ +2,147,483,647 pulse transfer value) *2, factory setting: 0 unit*1 | | | | | |
| ‡ 26 | #25 | H'4145 | \times | R/W | Running speed (| l) V(l) | Range: -2,147,483,648 ~ +2,147,483,647 unit*1 (10 ~ 200K PPS pulse transfer value) *2, factory setting: 1,000 unit*1 | | | | | |
| ‡ 28 | #27 | H'4147 | \times | R/W | Target position (II | I)P(II) | Range: -2,147,483,648 ~ +2,147,483,647 unit*1 (-2,147,483,648 ~ +2,147,483,647 pulse transfer value) *2, factory setting: 0 unit*1 | | | | | |
| # 30 | #29 | H'4149 | \times | R/W | Running speed | d (II) | Range: 0 ~ +2,147,483,647 unit*1 (10 ~ 200K PPS pulse | | | | | |
| | #31 | H'414B | \times | R/W | Running instruction factory setting: H | on '0000 | - - - - - - CLR output (On/Off) F PT - - | | | | | |
| bit # | | | | | | | Description Timing | | | | | |
| 0 | W | hen b[0] | =1, | Error | reset. Error indica | ator is o | off and FLAG in CR (CR#43.b[5]) is cleared to 0. 1 | | | | | |
| 1 | W | hen b[1] ternal si | =0- | →1 ,th | is is the same fun | ction a | is external input signal that forces to stop. When $0 \rightarrow 1$ | | | | | |
| 2 | W | hen b[2] | =1, | CW r | unning is forbidde | n, CW | running instruction is disabled. | | | | | |
| | When b[3]=1, CCW running is forbidden, CCW running instruction is disabled. 1 When b[3]=1, CCW running is forbidden, CCW running instruction is disabled. 1 | | | | | | | | | | | |
| 3 | VV | nen b[3] | =1, | CCW | running is forbidd | len, CC | CW running instruction is disabled. | | | | | |
| 3 4 5 | W | hen b[3] hen b[4] hen b[5] hen b[6] | =1, =1, =1, =0- | CCW CW p CCW | running is forbido ulse is generated pulse is generate | in JOC in JOC d in JC | CW running instruction is disabled. 1 G+ mode. 1 DG- mode. 1 Zero return is performed differently by the different | | | | | |
| 3 4 5 6 | Wi Wi Cu Pc po Pc Pc po Pc po | hen b[3] hen b[4] hen b[6] rrrent po ssition(1 int signa ssition(2 ssition(2 ssition(3 int signa ssition(4 int signa | =1, =1, =1, sitio): St al), [): St al), [): St al), [| CCW CW p CCW >1, zec n (CP art po DOG= art po DOG= art po DOG= art po DOG= | running is forbidd ulse is generated pulse is generated ro return is perfor) as the four follor sition (as the righ sition (as the righ Off and LSN (har sition (as the left Off and LSN (har | len, CC in JOC ed in JC med. 2 wing cc t pictur t pictur dware picture dware | CW running instruction is disabled. 1 G+ mode. 1 DG- mode. 1 Zero return is performed differently by the different onditions: 1 re below [1]) is at the right of zero point and DOG(near 0 \rightarrow 1 re below [2]) is at the right of zero point and DOG(near limit switch) signal=Off. 0 \rightarrow 1 below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 0 \rightarrow 1 | | | | | |
| JOG operation mode | VVI VVI VVI VVI VVI VVI VVI PC PC PC PC PC PC | hen b[3] hen b[4] hen b[5] hen b[6] rrrent poo ssition(1 ssition(2 ssition(2 ssition(3 ssition(4 | =1, =1, =1, =0- sitio): St al), I): St al), I): St al), I): St al), I | CCW p CCW p CCW p CCW and c CCW and | running is forbidd ulse is generated pulse is generated ro return is perfor) as the four follow sition (as the righ Off. sition (as the righ Off and LSN (har sition (as the left Off and LSN (har | ten, CC in JOC d in JC med. 2 t picture t picture t picture dware | CW running instruction is disabled. 1 G+ mode. 1 DG- mode. 1 Zero return is performed differently by the different onditions: 1 re below [1]) is at the right of zero point and DOG(near limit switch) signal=Off. 0→1 re below [2]) is at the left of zero point and DOG(near limit switch) signal=Off. 0→1 | | | | | |
| Time and the second sec | VVI VVI VVI VVI CU PC PC PC PC PC PC PC PC PC PC | hen b[3] hen b[4] hen b[5] hen b[6] rirent po sisition(1 sint sign: osition(3 sint sign: osition(4 int sign: d | =1, [=1, [=1, [=1, [=1, [=1, [=1, [=1, [| CCW p CCW p CCW p CCW -1, zec art po DOG= art po DOG= art po DOG= | running is forbidd ulse is generated pulse is generated ro return is perfor) as the four follow sition (as the righ Off. sition (as the righ Off and LSN (har sition (as the left Off and LSN (har sition (as the left Off and LSN (har void the righ off and LSN (har | ten, CCC in JOC di n JCC med. 2 t pictur t pictur t pictur t pictur dware | 2W running instruction is disabled. 1 3+ mode. 1 0G- mode. 1 Zero return is performed differently by the different onditions: 1 re below [1]) is at the right of zero point and DOG(near limit switch) signal=Off. 0→1 re below [4]) is at the left of zero point and DOG(near limit switch) signal=Off. 0→1 below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: the state of the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: the state of the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: the left of zero point return direction limit switch is grane=On. 0→1 Image: the left of zero point return direction limit switch is grane=On. 0→1 Image: the left of zero point return direction limit switch is grane=On. 0→1 Image: the left of zero point return direction limit switch is grane=On. 0→1 Image: the left of zero point return direction limit switch is grane=On. 0→1 Image: the left of zero point return direction limit switch is grane=On. 0→1 Image: the left of zero point return direction limit switch is grane=On. 0→1 Image: the left of zero point return direction limit switch is grane=On. 0→1 Image: th | | | | | |
| 2 4 5 6 4 5 9 6 | VVI VVI VVI CU PC PC PC PC PC PC PC PC PC PC PC PC PC | hen b[3] hen b[4] hen b[6] rrrent po ssition(1 ssition(2 ssition(3 ssition(3 ssition(4 int signa ssition(4 int signa ssition(4) int ssition(4) int ssition(4 | =1, [=1, [=1, [=1, [=1, [=1, [=1, [=1, [| CCW p CCW p CCW p CCW p CCW p CCW p c CCW c CCW p c CCW p CCW p CCW p CCW p CCW p c CCW p CCW p CCW p CCW p CCW p CCW p CCW p CCW p C CCW p C CCW p C CCW p CC | running is forbidd ulse is generated pulse is generated ro return is perfor) as the four follow sition (as the righ Off. sition (as the righ Off and LSN (har sition (as the left Off and LSN (har v)og | ten, CCC in JOC di n JCC med. z wing cc wing cc t pictur t pictur | CW running instruction is disabled. 1 G+ mode. 1 DG- mode. 1 Zero return is performed differently by the different onditions: 1 re below [1]) is at the right of zero point and DOG(near limit switch) signal=Off. 0→1 re below [3]) is at the left of zero point and DOG(near limit switch) signal=Off. 0→1 below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: Security of the security switch is the left of zero point return direction hardware polarity switch is grane=On. 0→1 Image: Security of the security of the security switch is relative position. 0→1 Image: Security of the security of the security security switch is relative position. 0→1 | | | | | |
| D S | VIII VIII VIII VIII VIII VIII VIII VI | hen b[3] hen b[4] hen b[5] hen b[6] rrent poo ssition(1 int signal ssition(2 ssition(2 ssition(2 ssition(4 int signal ssition(2 ssition(| =1, [=1, [=1, [=1, [=1, [=1, [=1, [=1, [| CCW p CCW p >1, zec CCW p >1, zec art po DOG= art po DOG= art po DOG= art po DOG= art po DOG= art po DOG= | running is forbido ulse is generated pulse is generated ro return is perfor) as the four follow sition (as the righ off. sition (as the righ off and LSN (har off and LSN (har off and LSN (har viog | ten, CCC in JOC din JCC med. 2 t pictur t pictur t pictur dware picture dware picture dware | 2W running instruction is disabled. 1 3+ mode. 1 0G- mode. 1 Zero return is performed differently by the different onditions: 1 re below [1]) is at the right of zero point and DOG(near limit switch) signal=Off. 01 re below [2]) is at the left of zero point and DOG(near limit switch) signal=Off. 01 below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 01 re below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 01 re below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 01 re below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 01 re below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 01 re below [4]) is at the left of zero point return direction hardware polarity switch list of the point switch li | | | | | |
| 0 8 7 10 00 operation mode 9 9 2 1 10 2 10 2 10 2 10 2 10 2 10 2 1 | Will Will Will Will Will Will Will Will Proposition Proposition Speee Joog_ | hen b[3] hen b[4] hen b[5] hen b[6] rrent poo ssition(1 int signal ssition(2 ssition(3 int signal ssition(4 int signal ssition(4 int signal ssition(2 int ssition(2 int ssition(2 | =1, , , , , , =1, , , =1, , =1, , =1, , =1, , =1, , =1, =1 | $\begin{array}{c} CCW & p \\ CW & p \\ CCW & p \\ \hline \\ CCW & p \\ \hline \\ ccw & p \\ cc$ | running is forbido ulse is generated pulse is generated ro return is perfor) as the four follow sition (as the righ Off. sition (as the righ Off and LSN (har sition (as the left Off and LSN (har sition (as the left Off and LSN (har viog vbias | ten, CC in JOC din JC med. 2 t pictur t pictur t pictur dware picture dware dware | CW running instruction is disabled. 1 G+ mode. 1 DG- mode. 1 Zero return is performed differently by the different onditions: 1 re below [1]) is at the right of zero point and DOG(near limit switch) signal=Off. 0→1 re below [2]) is at the left of zero point and DOG(near limit switch) signal=Off. 0→1 below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: the switch of the left of zero point return direction limit switch of the left of zero point return direction limit switch of the left of zero point return direction limit switch of the left of zero point return direction limit switch of the left of zero point return direction limit switch of the left of zero point return direction limit switch of the left of zero point return direction limit switch of the left of zero point return direction limit switch of the left of left of zero point return direction limit switch list of the left of left of zero point return direction list of the left of | | | | | |
| 100 100 <th100< th=""> <th100< th=""> <th100< th=""></th100<></th100<></th100<> | VIIII VIIII VIIII VIIII VIIII VIIII VIIII VIIII VIIIII VIIII VIIIII VIIII VIIIII VIIII VIIIII VIIII VIIIII VIIIII VIIIII VIIIII VIIIII VIIIII VIIIII VIIIII VIIIII VIIIII VIIIII VIIIII VIIIII VIIII VIIII VIIII VIIIII VIIII VIIII VIIII VIIII VIIII VIIII VIIII VIIII VIIII VIIII VIIIII | hen b[3] hen b[4] hen b[6] rrrent po ssition(2) ssition(3) ssition(2) ssition(3) ssition(2) ssition | =1, =1, =1, | CCW p CCW p CCW p CCW p CCW p CCW p ccw s art po DOG= art po DOG= art po DOG= \downarrow v v v v v v v v v v v v v v | running is forbido ulse is generated pulse is generated ro return is perfor) as the four follow sition (as the righ Off. sition (as the righ Off and LSN (har sition (as the left Off and LSN (har sition (as the left Off and LSN (har viog vbias | ten, CC in JOC di n JC med. z wing cc t pictur t pictur t pictur dware picture dware dware | 2W running instruction is disabled. 1 3+ mode. 1 0G- mode. 1 Zero return is performed differently by the different onditions: 1 re below [1]) is at the right of zero point and DOG(near limit switch) signal=Off. 01 re below [4]) is at the left of zero point and DOG(near limit switch) signal=Off. 01 below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 01 add the left of zero point return direction limit switch signal=On. 01 add the left of zero point return direction limit switch signal=On. 01 add the left of zero point return direction limit switch signal=On. 01 add the left of zero point return direction limit switch signal=On. 01 add the status (On/Off) is controlled by bf131 01 | | | | | |
| 3 4 5 6 7 8 10 12 13 12 13 12 13 12 13 13 14 | will Will Will Will Will Will Will Pc po Spee | hen b[3 hen b[4 hen b[6] rrent po ssition(2 ssition(2 ssition(3 ssition(2 ssition(2 ssition(3 ssition(2 ss | = 1, = 1, | $\begin{array}{c} CCW \\ CW \\ P \\ CCW \\ >1, zec \\ n (CF) \\ art po \\ DOG = \\ art po \\ DOG = \\ art po \\ OOG = \\ art po \\ OOG = \\ \hline \\ \\ \hline \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$ | running is forbidd ulse is generated pulse is generated pulse is generated ro return is perfor) as the four follow sition (as the righ Off. sition (as the righ Off and LSN (har sition (as the left Off and LSN (har istion (as the left Off and LSN (har off and LSN (har voide) we we we we we we we voide) we we we we we we we voide) we we we we we we we voide) we w | ten, CCC in JOC di n JCC med. z t pictur t pictur t pictur dware picture dware picture dware picture t pictur t | CW running instruction is disabled. 1 G+ mode. 1 DG- mode. 1 Zero return is performed differently by the different onditions: 1 re below [1]) is at the right of zero point and DOG(near limit switch) signal=Off. 0→1 re below [2]) is at the left of zero point and DOG(near limit switch) signal=Off. 0→1 below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: Second Comparison of the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: Second Comparison of the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: Second Comparison of the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: Second Comparison of the left of zero point return direction hardware polarity switch limit switch signal=On. 0→1 Image: Second Comparison of the left of limit switch of zero point limit switch of limit switch o | | | | | |
| 1 2 4 5 6 6 7 8 10 2 4 5 10 <th10< th=""> <th10< th=""> <th10< th=""> <</th10<></th10<></th10<> | Jog_ Jog_ VWI WI Cu Pc Pc Pc Pc Pc Pc Pc Pc Pc Pc V V V V V | hen b[3] hen b[4] hen b[5] hen b[6] rrent poo ssition(1 ssition(2 ssition(2 ssition(2 ssition(4 ssition(2 | =1, =1, =1, =1, =1, ==0- sitio): St al), [=0,]=0- 0]=0 2]=0- 0]=0 2]=0- 0]=0 2]=0- 0]=0 2]=0- 0]=0 2]=0- 0]=0 2]=0 2]=0 2]=0 2]=0 2]=0 2]=0 2]=0 | $\begin{array}{c} CCW \\ CW \\ P \\ CCW \\ >1, ze \\ art po \\ DOG = \\ art po \\ art po$ | running is forbido ulse is generated pulse is generated ro return is perfor) as the four follow sition (as the righ off. sition (as the righ Off and LSN (har istion (as the righ Off and LSN (har off and LSN (har with the left Off and LSN (har with the left) off and LSN (ha | ten, CC in JOC din JCC med. 2 t pictur t pictur t pictur dware dware dware dware t pictur t p | CW running instruction is disabled. 1 G+ mode. 1 DG- mode. 1 Zero return is performed differently by the different onditions: 1 re below [1]) is at the right of zero point and DOG(near limit switch) signal=Off. 01 re below [2]) is at the left of zero point and DOG(near limit switch) signal=Off. 01 re below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 01 re below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 01 re below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 01 re below [4]) is at the left of zero point and DOG (near limit switch) signal=On. 01 re below [4]) is at the left of zero point return direction 01 re below [2]) is at the left of zero point and DOG (near limit switch) signal=On. 01 re below [4]) is at the left of zero point return direction 01 re below [2]) is at the left of zero point and DOG (near limit switch) signal=On. 01 re below [4]) is at the left of zero point and DOG (near limit switch) signal=On. 0/1 re below [2]) is at the left of zero point and DOG (near limit switch) signal=On. 0/1 re below [2] or point si on. 0/1 re below [2] | | | | | |
| 1 1 <th1< th=""> <th1< th=""> <th1< th=""> <th1< th=""></th1<></th1<></th1<></th1<> | Will Will Will Will Will Will Proposition Proposition Speee | hen b[3] hen b[4] hen b[6] rrent poo ssition(1 int sign: ssition(4 int sign: ssition(4 int sign: ssition(4 int sign: ssition(4 int sign: ssition(4 int sign: ssition(2 ssition(2 int sign: ssition(2 int ssition(2 int s | = 1, | CCW p CCW p CCW p CCW p CCW p CCW p ccw s ccw s c | running is forbidd ulse is generated pulse is generated ro return is perfor) as the four follow sition (as the righ Off. sition (as the righ Off and LSN (har sition (as the left Off and LSN (har | ten, CC in JOC di n JC med. 2 t pictur t pictur t pictur dware picture dware dware t pictur t pictur t pictur t pictur dware dware t pictur t pictu | 2W running instruction is disabled. 1 3+ mode. 1 0G- mode. 1 Zero return is performed differently by the different onditions: 1 re below [1]) is at the right of zero point and DOG(near limit switch) signal=Off. 01 re below [2]) is at the left of zero point and DOG(near limit switch) signal=Off. 01 below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 01 add the status (Drive point (1/2) are point return direction limit switch) signal=On. 01 add the status (On/Off) is controlled by b[13]. 01 add the status (On/Off) is controlled by b[13]. 0/1 and the status (On/Off) is controlled by b[13]. 0/1 add the status (On/Off) is controlled by b[13]. 0/1 add the status (On/Off) is controlled by b[13]. 0/1 add the status (On/Off) is controlled by b[13]. 0/1 add the status (On/Off) is controlled by b[13]. 0/1 | | | | | |
| 3 4 5 6 6 10 0 0 0 10 12 13 13 13 13 13 13 13 13 | Will Will Will Will Will Will Will Pc po Pc po Pc po Spee | hen b[3 hen b[4 hen b[6] rrent po rsition(1 int signa position(3 int signa position(4 int signa position(4 int signa position(4 int signa position(4 int signa position(4 int signa position(4 int signa position(7 hen b[7 hen b[1 then b[1 | = 1, | $\begin{array}{c} CCW \\ CW \\ P\\ CCW \\ P\\ $ | running is forbido ulse is generated pulse is generated pulse is generated ro return is perfor) as the four follow sition (as the righ Off. sition (as the righ Off and LSN (har sition (as the left Off and LSN (har sition (as the left Off and LSN (har viog vbias | ten, CC in JOC di n JC med. 2 wing cc t pictur t pictur t pictur dware picture dware dware | 2W running instruction is disabled. 1 3+ mode. 1 0G- mode. 1 Zero return is performed differently by the different onditions: 1 re below [1]) is at the right of zero point and DOG(near limit switch) signal=Off. 01 re below [2]) is at the left of zero point and DOG(near limit switch) signal=Off. 01 below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 01 at the left of zero point return direction limit switch signal=On. 01 at the left of zero point return direction limit switch signal=On. 01 at the left of zero point return direction limit switch signal=On. 01 at the left of zero point return direction limit switch signal=On. 01 at the left of zero point return direction limit switch signal=On. 01 at the return direction limit switch signal=On. 01 at the return direction limit switch limit | | | | | |
| 3 4 5 6 9 10 | will Will Will Will Will Will Will Will Cu Pc Pc Pc W | hen b[3 hen b[4 hen b[6] rrent po sistion(2 sistion(2 sistion(3 sint signa sistion(4 int signa sistion(4 int signa sistion(4 int signa sistion(4 int signa sistion(4 int signa sistion(4 int signa sistion(4 int signa sistion(1 hen b[7 hen b[1 hen b[7 hen b[1 hen h | = 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, | $\begin{array}{c} CCW \\ CW \\ P \\ CCW \\ >1, zec \\ n (CF) \\ art po \\ DOG = \\ art po \\ DOG = \\ art po \\ OOG = \\ art po \\ OOG = \\ art po \\ on c \\ r \\ vm $ | running is forbidd ulse is generated pulse is generated pulse is generated ro return is perfor) as the four follow sition (as the righ Off. sition (as the righ Off and LSN (har sition (as the righ Off and LSN (har sition (as the left Off and LSN (har with a constant) off and LSN (har with a constant) with a constant with a constant) with a constant with a constant with a constant with a constant with a constant with a constant with a constant a c | ten, CC in JOC di n JC med. z wing cc wing cc t pictur t pictur t pictur t pictur dware picture dware picture dware picture t pictur t pic | CW running instruction is disabled. 1 G4 mode. 1 DG- mode. 1 Zero return is performed differently by the different onditions: 1 re below [1]) is at the right of zero point and DOG(near limit switch) signal=Off. 0→1 re below [3]) is at the left of zero point and DOG(near limit switch) signal=Off. 0→1 below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: the state left of zero point and DOG(near limit switch) signal=On. 0→1 Image: the state left of zero point and DOG(near limit switch) signal=On. 0→1 Image: the state left of zero point and DOG(near limit switch) signal=On. 0→1 Image: the state left of zero point and DOG(near limit switch) signal=On. 0→1 Image: the state left of zero point return direction limit switch signal=On. 0→1 Image: the state left of zero point limit switch signal=On. 0/1 Image: the state left of zero point and DOG(near limit switch) signal=On. 0/1 Image: the state left of zero point and DOG (near limit switch) signal=On. 0/1 Image: the state left of zero point and DOG (near limit switch) signal=On. 0/1 Image: the state left of zero point and DOG (near limit switch) signal=On. 0/1 Image: the state left o | | | | | |
| 3 4 2 4 2 4 2 5 6 3 4 5 5 5 7 8 10 3 4 5 5 10 <th10< th=""> 10 <th10< th=""> <th10< td="" th<=""><td>Will Will Will Will Will Will Will Will Cu Pc po Pc Pc po Pc po Pc po Pc po Pc po Pc po Spee Will W W W W WW W WW W WW W WW W WW W WW WW WW WW WW WW</td><td>hen b[3 hen b[4 hen b[6] rrent po ssition(2 ssition(2 ssition(3 ssition(4 ss</td><td>=1,</td><td>$\begin{array}{c} CCW \\ CW \\ P \\ CCW \\ >1, zec \\ n (CF) \\ art po \\ DOG = \\ art po \\ DOG = \\ art po \\ OOG = \\ art po \\ OOG = \\ \hline \\ w \\ w$</td><td>running is forbidd ulse is generated pulse is generated pulse is generated ro return is perfor) as the four follow sition (as the righ Off. sition (as the righ Off and LSN (har sition (as the left Off and LSN (har istion (as the left Off and LSN (har with a constant) off and LSN (har with a constant) with a constant sition (as the left off and LSN (har with a constant) with a constant) solute position. at running by the current position (C soutput s 130ms t al error counter. a coutput point is C Work mode factory setting: H'0001</td><td>ten, CC in JOC di n JC med. z t pictur t pictur t pictur dware picture dware picture dware picture dware picture t pictur t pictu</td><td>2W running instruction is disabled. 1 3+ mode. 1 0G- mode. 1 Zero return is performed differently by the different onditions: 1 re below [1]) is at the right of zero point and DOG(near limit switch) signal=Off. 0→1 re below [2]) is at the left of zero point and DOG(near limit switch) signal=Off. 0→1 below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 0→1 are below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 0→1 are below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 0→1 are below [4]) is at the left of zero point and DOG (near limit switch) signal=On. 0→1 are below [4]) is at the left of zero point return direction Hardware polarity switch are below [4]) is at the left of zero point and DOG (near limit switch) signal=On. 0→1 are below [4]) is at the left of zero point return direction Hardware polarity switch are below [7] or point [1] 1 1 are below [7] or point [1] 1 1 are below [7] or point [7] 1 1 are below [7] or point [7] 1 1 are below [7] or point [7] 1 1 1</td></th10<></th10<></th10<> | Will Will Will Will Will Will Will Will Cu Pc po Pc Pc po Pc po Pc po Pc po Pc po Pc po Spee Will W W W W WW W WW W WW W WW W WW W WW WW WW WW WW WW | hen b[3 hen b[4 hen b[6] rrent po ssition(2 ssition(2 ssition(3 ssition(4 ss | =1, | $\begin{array}{c} CCW \\ CW \\ P \\ CCW \\ >1, zec \\ n (CF) \\ art po \\ DOG = \\ art po \\ DOG = \\ art po \\ OOG = \\ art po \\ OOG = \\ \hline \\ w \\ w$ | running is forbidd ulse is generated pulse is generated pulse is generated ro return is perfor) as the four follow sition (as the righ Off. sition (as the righ Off and LSN (har sition (as the left Off and LSN (har istion (as the left Off and LSN (har with a constant) off and LSN (har with a constant) with a constant sition (as the left off and LSN (har with a constant) with a constant) solute position. at running by the current position (C soutput s 130ms t al error counter. a coutput point is C Work mode factory setting: H'0001 | ten, CC in JOC di n JC med. z t pictur t pictur t pictur dware picture dware picture dware picture dware picture t pictur t pictu | 2W running instruction is disabled. 1 3+ mode. 1 0G- mode. 1 Zero return is performed differently by the different onditions: 1 re below [1]) is at the right of zero point and DOG(near limit switch) signal=Off. 0→1 re below [2]) is at the left of zero point and DOG(near limit switch) signal=Off. 0→1 below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 0→1 are below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 0→1 are below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 0→1 are below [4]) is at the left of zero point and DOG (near limit switch) signal=On. 0→1 are below [4]) is at the left of zero point return direction Hardware polarity switch are below [4]) is at the left of zero point and DOG (near limit switch) signal=On. 0→1 are below [4]) is at the left of zero point return direction Hardware polarity switch are below [7] or point [1] 1 1 are below [7] or point [1] 1 1 are below [7] or point [7] 1 1 are below [7] or point [7] 1 1 are below [7] or point [7] 1 1 1 | | | | | |
| 2 10 00 </td <td>Wi Wi Wi Wi Wi Wi Cu Pc Pc</td> <td>hen b[3] hen b[4] hen b[5] hen b[6] rrent poo ssition(1 int signal ssition(2 ssition(2 ssition(2 ssition(4 int signal ssition(4 int signal ssition(4 int signal fhen b[7 /hen b[1 /hen b[1 /hen b[1 /hen b[1 /hen b[1</td> <td>= 1, 1,</td> <td>CCW p CCW p CCW p CCW p CCW p CCW p CCW p c CCW c c c c c c c c c c c c c c</td> <td>running is forbidd ulse is generated pulse is generated ro return is perfor) as the four follow sition (as the righ Off. sition (as the righ Off and LSN (har off and LSN (har off and LSN (har control (as the left) Off and LSN (har off and LSN (har witten (as the left) off and LSN (har sitten (as the left) off and LSN (har witten (as</td> <td>ten, CC in JOC di n JC med. z t pictur t pictur</td> <td>CW running instruction is disabled. 1 G+ mode. 1 OG- mode. 1 Zero return is performed differently by the different onditions: 1 re below [1]) is at the right of zero point and DOG(near limit switch) signal=Off. 0→1 re below [2]) is at the left of zero point and DOG(near limit switch) signal=Off. 0→1 re below [3]) is at the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: switch is grane on the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: switch is grane on the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: switch is grane on the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: switch is grane on the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: switch is grane on the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: switch is grane on the left of zero point and DOG(near limit switch) signal=On. 0/1 Image: switch is grane on the left of zero point and DOG (near limit switch) signal=On. 0/1 Image: switch is grane on the left of zero point and DOG (near limit switch) signal=On. 0/1 Image: switch is grane on the left of zero point and DOG (near limit switch) signal=On. 0/1 Image: switch is gr</td> | Wi Wi Wi Wi Wi Wi Cu Pc | hen b[3] hen b[4] hen b[5] hen b[6] rrent poo ssition(1 int signal ssition(2 ssition(2 ssition(2 ssition(4 int signal ssition(4 int signal ssition(4 int signal fhen b[7 /hen b[1 /hen b[1 /hen b[1 /hen b[1 /hen b[1 | = 1, 1, | CCW p CCW p CCW p CCW p CCW p CCW p CCW p c CCW c c c c c c c c c c c c c c | running is forbidd ulse is generated pulse is generated ro return is perfor) as the four follow sition (as the righ Off. sition (as the righ Off and LSN (har off and LSN (har off and LSN (har control (as the left) Off and LSN (har off and LSN (har witten (as the left) off and LSN (har sitten (as the left) off and LSN (har witten (as | ten, CC in JOC di n JC med. z t pictur t pictur | CW running instruction is disabled. 1 G+ mode. 1 OG- mode. 1 Zero return is performed differently by the different onditions: 1 re below [1]) is at the right of zero point and DOG(near limit switch) signal=Off. 0→1 re below [2]) is at the left of zero point and DOG(near limit switch) signal=Off. 0→1 re below [3]) is at the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: switch is grane on the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: switch is grane on the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: switch is grane on the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: switch is grane on the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: switch is grane on the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: switch is grane on the left of zero point and DOG(near limit switch) signal=On. 0/1 Image: switch is grane on the left of zero point and DOG (near limit switch) signal=On. 0/1 Image: switch is grane on the left of zero point and DOG (near limit switch) signal=On. 0/1 Image: switch is grane on the left of zero point and DOG (near limit switch) signal=On. 0/1 Image: switch is gr | | | | | |
| 3 4 5 6 9 9 10 <th10< th=""> <th10< th=""> <th10< th=""></th10<></th10<></th10<> | Will Will Will Will Will Will Will Will Pc po Pc po Pc po Pc po Pc po Pc po Spece Spece JOG Will W Will W Will WW Will WW Will # 322 #32 | hen b[3] hen b[4] hen b[6] rrent poo ssition(1 int sign: ssition(2 | = 1, | CCW p CCW p C | running is forbidd ulse is generated pulse is generated pulse is generated ro return is perfor) as the four follow sition (as the righ Off. sition (as the righ Off and LSN (har istion (as the righ Off and LSN (har off and LSN (har off and LSN (har with and LSN (har ax with and LSN (har with and LSN | ten, CC in JOC di n JC med. 2 t pictur t pictur t pictur dware picture dware dware t pictur t | CW running instruction is disabled. 1 G+ mode. 1 QG- mode. 1 Zero return is performed differently by the different onditons: 1 re below [1]) is at the right of zero point and DOG(near limit switch) signal=Off. 01 re below [3]) is at the left of zero point and DOG(near limit switch) signal=Off. 01 re below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 01 Imit switch) signal=On. Hardware polarity switch Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) signal=On. Imit switch) sig | | | | | |
| 3 4 5 6] Old obstration mode 10 10 7 8 10 12 11 13 | will Will Will Will Will Will Will Will Cu Pc Pc Pc P | hen b[3 hen b[4 hen b[6] rrent po rsition(2 rsition(2 rsition(3 rsition(2 rsition(3 rsition(2 rsition(3 rsition(2 rsition(2 rsition(2 rsition(2 rent signa rsition(2 rsition(2 rent signa rsition(2 rent signa rsition(2 rent signa rsition(2 rent signa rsition(2 rent signa rsition(2 rent signa rsition(2 rent signa rsition(2 rent signa rsition(2 rent signa rsition(2 rent signa rsition(2 r | = 1, 1, | CCW p CCW p CCW p CCW p CCW p CCW p CCW p ccw s ccw s c | running is forbidd ulse is generated pulse is generated pulse is generated ro return is perfor) as the four follow sition (as the righ Off. sition (as the righ Off and LSN (har sition (as the left Off and LSN (har isition (as the left Off and LSN (har viog viog votes viog votes bsolute position. art running by the current position (C a outputs 130ms t nal error counter. as coumon outp a output point is C Work mode factory setting: H'0001 | ten, CC in JOC di n JC med. z wing cc wing cc wing cc t pictur t pictur t pictur t pictur dware picture dware work n P) is c o Serv ut poin ff. Whe bits bit | CW running instruction is disabled. 1 G+ mode. 1 DG- mode. 1 Zero return is performed differently by the different onditors: re below [1]) is at the right of zero point and DOG(near limit switch) signal=Off. 0→1 re below [2]) is at the left of zero point and DOG(near limit switch) signal=Off. 0→1 below [4]) is at the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: Switch is a stable left of zero point and DOG(near limit switch) signal=On. 0→1 Image: Switch is a stable left of zero point return direction that ware polarity switch is polarity switch is grane polarity switch is polarity switch is grane polarity switch is relative position. 0/1 Image: Switch is relative position. 0/1 0/1 0 Image: Switch is relative position. 0/1 0/1 0 Image: Switch is polarity point is On. 0/1 0/1 0 Image: Switch is polarity point is On. 0/1 0/1 0 Image: Switch is polarity point is On. 0/1 0 0 0 Image: Switch is polarity polarit | | | | | |
| 3 4 5 6 9 1 1 1 1 1 1 1 1 1 1 1 1 | Will Will Will Will Will Will Cu Pc po Pc Pc po Pc po Pc po Pc po Spee Will # WW WW WW WW WW WW WW WW WW WW WW | hen b[3] hen b[4] hen b[6] rrent poo ssition(1 int signal ssition(2 ssition(| =1, [=1, =1, == 0] =1, == 0 = 0 = 0 = 0 = 0 = 0 = 0 = 0 = 0 = | CCW p CCW p C | running is forbidd ulse is generated pulse is generated ro return is perfor) as the four follow sition (as the righ Off. sition (as the righ Off and LSN (har isition (as the righ Off and LSN (har off and LSN (har with and LSN (| ten, CC in JOC di n JC med. z wing cc wing cc t pictur t pictur t pictur t pictur dware picture dware picture dware picture t pictur t pic | CW running instruction is disabled. 1 G+ mode. 1 DG- mode. 1 Zero return is performed differently by the different onditions: re below [1]) is at the right of zero point and DOG(near limit switch) signal=Off. 0→1 re below [2]) is at the left of zero point and DOG(near limit switch) signal=Off. 0→1 re below [3]) is at the left of zero point and DOG(near limit switch) signal=On. 0→1 Image: switch signal=Off. re point return direction limit switch) signal=On. Hardware polarity switch Image: switch signal=On. Image: switch signal=On. Image: switch switch signal=On. Image: switch switch signal=On. Image: switch switch switch switch switch signal=On. Image: switch swi | | | | | |



| ~ | | | | | |
|------------|--|--|--|--|--|
| 0 | W | nen b[0] | =0, : | syster | n i |
| 1 | W | nen b[1] | =1, · =1 · | CW pi | uls nu |
| 2 | W | nen b[3] | = 1, 1 = 1, 1 | zero re | etu |
| 3 | b[3 |] will be | cle | ared to | 0 (|
| 4 | Wh | nen b[4] | =1, ' | Curre | ent |
| 5 | aga W/F | ain or co | omp =1 | PLL or | |
| 0 | W | ien PU | star | ts to e | xe |
| 6 | zei | ro return | n or | positic | n |
| - | Wł | nen PU | is ru | nning | , S |
| 1 | tha | at PU is | pau | se and | |
| 9 | Wł | nen h[9]: | =1 i | it mea | ns |
| 10 | 10/1 | on b[10 | 1, 1 | it mo | 0 |
| 10 | | | י –ני י | , it me | aı |
| | #44 | H'4158 | \times | R | |
| | | | | | F |
| | #45 | H'4159 | \times | R/W | n |
| | | | | | |
| | #10 | L1/1 = A | \sim | DAA/ | E |
| | #40 | п415A | \times | r/// | |
| • | Innut | onerati | on c | nf man | U.S |
| - | 1.Ma | nual pu | lse d | genera | ato |
| | 2.2-p | hase(A | pha | se/B p | bh |
| | is | as follo | NS: | | |
| | \cap | \ | | | |
| | (m) | D A pi | nase | 9 | |
| | 】 |) ` | Г | | Г |
| | | $ \mathcal{V} $ | I | пп | |
| | \cup | / B ph | nase | | |
| | 3.Wr | ien it ari | ives | s the (I | LS |
| | LS | SN is ON | 1, C | W is a | llo |
| | 4 Po | aition or | | | |
| | | SILION CC | mpi | ete ind | dic |
| | (C | R#43 b | 6=0 | ete in n). | dic |
| | (C 5.Th | R#43 bi e PU ou | tput | ete in n). pulse | dic , p |
| | (C 5.The Cf | R#43 b PU ou R#46) a | fmpi 6=0 tput re pi | ete in n). pulse roporti | dic , p ior |
| #48 | (C 5.The CF #47 | R#43 bi PU ou R#46) ai H'415B | tput | n). pulse roporti | dic , r ior |
| #48 | (C 5.Th CF #47 | R#43 bi PU ou R#46) ai H'415B | tput | ete in pulse roporti R/W | dic , f |
| #48 | (C 5.The CF #47 #47 | R#43 bi e PU ou R#46) ai H'415B | fre pi | ete in pulse roporti R/W | dic , r ior |
| #48 #50 | (C 5.The CF #47 #49 | R#43 bi e PU ou R#46) ai H'415B | $\approx 10^{10}$ | ete ind n). pulse roporti R/W | dic , r ior |
| #48 #50 | (C 5.The CF #47 #49 | R#43 bi e PU ou R#46) ai H'415B H'415D | fre pi | ete ind n). pulse roporti R/W | dic , r ior |
| #48 #50 | (C 5.The CF #47 #49 | R#43 bl e PU ou R#46) al H'415B | fre pi | ete ind n). pulse roporti R/W R/W | dic , r ior |
| #48 #50 | (C 5.Th CF #47 #49 | R#43 bi e PU ou R#46) ar H'415B | tput fe put × | ete ind n). pulse roporti R/W R/W | dic ;, r ior |
| #48 | (C 5.The CF #47 #49 | R#43 bi e PU ou R#46) ai H'415B | find for the second sec | R/W | dic , F ior |
| #48 #50 | (C 5.The CF #47 #49 #51 | H'415F | freput fe=0 tput reput × | R/W | |
| #48 | (C 5.The CF #47 #49 #51 | H'415F | impi 6=0 tput x x | R/W | dic ;, f ior |
| #48 | (C 5. Tho CF #47 #49 #51 | H'415D H'415F | tput re pi | R/W | dic , F ior |
| #48 #50 | (C 5. Tho CF #47 #49 #51 | H'415D H'415F | tput | R/W | |
| #48 #50 | (C 5.Tha CF #47 #49 #51 | H415b H445b H415b H415b | tput re pi | R/W | dic s, f ior |
| #48 #50 | #47 #47 #51 | H'415D H'415F | The picture pi | R/W | |
| #48 | #49 #51 | H'415D H'415F | | R/W | |
| #48 | #49 #51 | H'415D H'415F | tre pi × × | R/W | |
| #48 #50 | #47 #49 #51 | H'415D H'415F | tpot tpot × × × | R/W | |
| #48 | #47 #49 #51 | H'415D H'415F | trepit | R/W | |
| #48 | (C 5.Tho <u>CF</u> #47 #49 #51 | H'415F | mpo maartine maartine maartine N N N N N N N N N N N N N N | R/W | |
| #48 | #47 #49 #51 | H'415D H'415F H'4160 | mpo mo to the pi X X X X X X | R/W | |
| #48 | #47 #47 #49 #51 | H'415D H'415F H'4160 | mpo top top top top top top top top top top | R/W | |
| #48 | #47 #47 #49 #51 | H'415D H'415F H'4160 | The product of the pr | R/W | |
| #48 | (C 5.Tho CF #47 #49 #51 | H'415D H'415F H'4160 | mpo manufase trep X X X X X X X X X X X X X X X X X X X | R/W | |
| #48 | (C 5.Tho CF #47 #49 #51 #52 | H'415D H'415D H'415F H'4160 | mpo mage tup pi X X X X X X X X X X X X X X X X X X X | R/W | |
| #48 | (C 5.Tho CF #47 #49 #51 #52 #53 | H'415D H'415D H'415F H'4160 H'4160 | mpo ga=ut pel × × × × × × | R/W R/W R/W R/W R/W | |
| #48 | (C 5.Tho CF #47 #49 #51 #52 #53 | H'415D H'415D H'415F H'4160 H'4161 | mpo ga=uti pe pi X X X X X X X X X X X X X X X X X X X | R/W R/W R/W R/W R/W | |
| #48 #50 | (C 5.Tho CF #47 #49 #51 #51 #52 #53 Jnit s | H'415D H'415D H'415F H'4160 H'4160 H'4161 | and a constraint of the point | R/W R/W R/W R/W R/W R/W | diction of the second sec |
| #48 #50 | (C 5.Tho CF #47 #49 #51 #52 #53 Unit s Conve | H'415D H'415D H'415F H'4160 H'4160 H'4161 | r_{6} = put r_{2} \times \times \times \times \sim | R/W R/W R/W R/W R/W R/W R/W | diction of the second sec |
| #48 #50 | (C 5.Tho CF #47 #49 #51 #52 #53 Unit s Conve outpu | H'415D H'415D H'415F H'4160 H'4160 H'4161 H'4161 | angonini Garutine X X X X X X X X X X X X X X X X X X X | R/W R/W R/W R/W R/W R/W R/W | dicities and the second s |
| #48 #50 | (C 5.Tho CF #47 #49 #51 #52 #53 Juit s Convector | H'415D H'415D H'415F H'415F H'4160 H'4160 H'4161 H'4161 | accordination of the second s | R/W R/W R/W R/W R/W R/W R/W R/W | dicities and the second |

It indicates DVP-01PU hardware malfunction or error parameter setting when error LED flashes. ERR code is recorded in CR#44.

| Error code | Description | Error code | Description |
|------------|---|------------|--|
| H'0000 | No error | H'0013 | Zero return deceleration (V_{RT}) setting error |
| H'0001 | Target address (I) setting error | H'0014 | JOG speed (V _{JOG}) setting error |
| H'0002 | Target address (II) setting error | H'0020 | CW pulse is forbidden |
| H'0010 | Running speed (I) setting error | H'0021 | CCW pulse is forbidden |
| H'0011 | Running speed (II) setting error | H'0080 | Hardware error in internal memory |
| H'0012 | Zero return deceleration (V_{CR}) setting error | H'0081 | Data write in error in internal memory |

Description is ready. When b[0]=1, PU is executing position control mode (Pulse is outputting). se is outputting.

llse is outputting.

urn is compared. b[3] is cleared to 0 by user program. When PU is power on again, 0 automatically.

position CP(PLS)"(CR#34, #33), that is 32 bit, is overflow. When PU is power on preturn, b[4] will be cleared to 0 automatically.

occurred. Error code is stored in CR#44.

ecute zero return or error reset (only when error occurred), it will clear b[6] to 0. When control is completed, it will set b[6] to 1.

TOP status is on. PU will stop output, and b[7] will be set to 1 at this time. It means will execute the uncompleted route and b[7] will be cleared to 0 after STOP status is

manual pulse generator inputs with counting upward.
In manual pulse generator inputs with counting downward

| e manaal palee generater inpate mar eeaning de maraia. | | | | | | |
|--|---|--|--|--|--|--|
| | | | | | | |
| Error code | Please refer to "Error Code & Troubleshooting" for detail. Factory setting: H'0000 | | | | | |
| lectronic gearing umerator of MPG input | Please refer to the following description. Factory setting: H'XXXX | | | | | |
| lectronic gearing denominator of | Please refer to the following description. Factory setting: H'XXXX | | | | | |

al pulse generator:

or input operation is ON when b5 of CR#32 is set to 1.

ase) can be input from the manual pulse generator to $\ \Phi A$ and $\ \Phi B$ FP/RP I/O pulse

| L Input pulse X CR#45 CR#46 = output pulse RP | | | | | |
|---|--------------------------------|--|--|--|-------------|
| | L > T > | Input pulse X CR#45 = output pulse | | | Servo motor |

SP/LSN) limit, output will stop immediately. When LSP is ON, CCW is allowed. When wed. The position complete flag is not turned ON.

cation (CR#43, b6=Off). When position is done, the actual operation is expressed:

pulse frequency of manual pulse generator, and the electronic gearing (CR#45,

| al lo each other. | | | | | | |
|--|--|--------------------------|--|--|--|--|
| Input frequency of manual pulse generator | The input frequency of manual pulse generator, factory setting: | | | | | |
| accumulated pulse number of manual pulse generator | The count value of CW manual pulse input is " +" symbol, on the contrary, the CCW manual pulse input is "-"symbol. And the count value is nothing to do with the ratio setting of manual electronic gearing (CR#45, #46). Factory setting: 0. | | | | | |
| Response speed of manual pulse generator | Value | Response speed | When response speed setting is faster, the commands of pulse output and manual pulse generator input | | | |
| | ≧5 | 4ms (factory setting) | | | | |
| | 4 | 32ms | response speed setting is slower, the | | | |
| | 3 | 108ms | command of pulse output is slower than the command of manual pulse generator input. Factory setting: 5 | | | |
| | 2 | 256ms | | | | |
| | 1or 0 | 500ms | | | | |
| | | | | | | |
| | bit # | Status | Description | | | |
| | b0 | START input | When START input is On, b0 is On | | | |
| | b1 | STOP input | When STOP input is On, b1is On | | | |
| Terminal status | b2 | DOG input | When DOG input is On, b2 is On | | | |
| | b3 | PG0 input | When PG0 input is On, b3 is On | | | |
| | b4 | LSP input | When LSP input is On, b4is On | | | |
| | b5 | LSN input | When LSN input is On, b5 is On | | | |
| | b6 | A phase input | When A phase input is On, b6 is On | | | |
| | b7 | B phase input | When B phase input is On, b7 is On | | | |
| | b8 | CLR output | When CLR output is On, b8 is On | | | |
| System version | System version: in hexadecimal system. Ex.: H'0100, the software version is V1.00. | | | | | |

to the varying of " b0, b1" unit setting of CR#5.

pulse unit: (1) output the maximum pulse if it exceeds the maximum range. (2) the fit's lower than the minimum range.

Error Code & Troubleshooting